ABSTRACT OF THE DISCLOSURE

A solid-state image sensing apparatus having output channels CH1 and CH2 has first and second driving modes in which pixel signals of pixels in the same image sensing area are read out. The number of output channels to be used is changed between the first driving mode and the second driving mode. In at least one of the two modes, the phase of the read timing of pixel signals of pixels adjacent in the horizontal direction is shifted by a predetermined amount.

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